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IN THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US)

Applicant: KENNINGTON, Peter

International  
Application No.: PCT/GB99/00695 Express Mail Label No. EL602995655US

International  
Filing Date: 08 March 1999 Date of Deposit: 06 September 2000

Title: PREDISTORTER

Box PCT  
Assistant Commissioner for Patents  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

RECEIVED  
JAN 03 2002  
Technology Center 2600

Dear Sir:

Please preliminarily amend the national phase entry of the above-identified application as follows:

In the Claims

Please delete claims 1-18 and insert the following new claims:

--19. A circuit for deriving a third order signal from an input signal, comprising input circuitry for providing an input signal to the circuit along first, second and third paths, a first combiner for combining the input signal from the first and second paths to produce a second order signal on a squared signal path, a filter for low-pass filtering the second order signal to remove components at the frequency of the input signal and harmonics thereof, and a second combiner for combining the filtered second order signal from the squared signal path with the input signal from the third path to produce a third order signal.

20. A circuit as claimed in claim 19, for additionally deriving a fifth order signal from an input signal, further comprising circuitry for providing the second order signal along a second squared signal path, and a third combiner for combining the second order signal from the second squared signal path with the third order signal to produce a fifth order signal.